

CLAIM LISTING

1. (Currently Amended) A method for displaying pictures, said method comprising:

fetching a portion of a picture being stored in a frame buffer, the portion of the picture stored with a byte order;

converting the byte order of the portion of the picture to a predetermined byte order, the byte order being different from the predetermined byte order; ~~and~~

storing the portion of the picture in another buffer with the predetermined byte order; and

providing an indicator indicating whether the byte order is different or opposite from the predetermined order that the portion of the picture is converted.

2. The method of claim 1, wherein the predetermined order is selected from a group consisting of big endian byte order and little endian byte order.

3. (Cancelled)

4. (Currently Amended) The method of claim [3], further comprising:

swapping a first pixel from the portion of the picture and a second pixel from the portion of the picture if the indicator indicates that the byte order is different or opposite from the predetermined order; and

swapping a third pixel from the portion of the picture and a fourth pixel from the portion of the picture if the indicator indicates that the byte order is different or

opposite from the predetermined order.

5. (Currently Amended) A system for displaying pictures, said system comprising:

a first circuit for fetching a portion of a picture stored in a frame buffer, the portion of the picture being stored with a byte order;

a second circuit for converting the byte order of the portion of the picture to a predetermined byte order, the byte order being different from the predetermined byte order; and

a buffer for storing the portion of the picture with the predetermined byte order; and

a state machine for providing an indicator indicating whether the byte order is different or opposite from the predetermined order that the portion of the picture is converted to the second circuit.

6. The system of claim 5, wherein the predetermined order is selected from a group consisting of big endian byte order and little endian byte order.

7. (Cancelled).

8. (Currently Amended) The system of claim 5 [7], wherein the second circuit further comprises:

a first multiplexer for selecting one of a first pixel of the portion of the picture and a second pixel of the portion of the picture;

a second multiplexer for selecting another of the first pixel of the portion of the picture and a second

pixel of the portion of the picture, from the first multiplexer;

a third multiplexer for selecting one of a third pixel of the portion of the picture and a fourth pixel of the portion of the picture;

a fourth multiplexer for selecting another of the third pixel of the portion of the picture and the fourth pixel of the portion of the picture, from the third multiplexer;

a fifth multiplexer for multiplexing outputs from the first multiplexer, the second multiplexer, the third multiplexer, and fourth multiplexer; and

the selections of the first multiplexer, the second multiplexer, the third multiplexer, and the fourth multiplexer being controlled by the indicator provided by the state machine.

9. (Original) A method for displaying pictures, said method comprising:

fetching a portion of a picture stored in a frame buffer, the portion of the picture being stored with a pixel order;

converting the pixel order of the portion of the picture to a predetermined pixel order; and

storing the portion of the picture in another buffer with the predetermined pixel order.

10. (Original) The method of claim 9, further comprising:

rearranging a plurality of pixels from the portion of

the picture in a plurality of different pixel orders;

receiving an indicator indicating the pixel order; and
selecting the pixels rearranged in one of the
plurality of different pixel orders based on the indicator
indicating the pixel order.

11. (Original) A system for displaying pictures, said system comprising:

an input data write unit for fetching a portion of a picture stored in a frame buffer, the portion of the picture being stored with a pixel order;

a circuit for converting the pixel order of the portion of the decoded picture to a predetermined pixel order; and

a buffer for storing the portion of the picture with the predetermined pixel order.

12. (Original) The system of claim 11, wherein the circuit further comprises:

a demultiplexer for separating a plurality of pixels from the portion of the picture;

a plurality of multiplexers for combining the separated plurality of pixels in a corresponding plurality of pixel orders; and

another multiplexer for selecting an output from one of the plurality of multiplexers, based on an indicator indicating the pixel order provided by the state machine.

13-23. (Cancelled).